

**SIDDHARTH INSTITUTE OF ENGINEERING & TECHNOLOGY::PUTTUR****(Autonomous)**

(Approved by AICTE, New Delhi & Affiliated to JNTUA, Anantapuramu)

(Accredited by NBA & Accredited by NAAC with 'A+' Grade)

(An ISO 9001:2008 Certified Institution)

Siddharth Nagar, Narayanavanam Road, PUTTUR-517 583

Examinations Branch**TIME TABLE****M.Tech I Year II Semester (R20) Regular & Supplementary Examinations July-2025****TIMINGS: 09:30 AM to 12:30 PM**

Date/Day	STRUCTURAL ENGINEERING	COMPUTER SCIENCE AND ENGINEERING	THERMAL ENGINEERING	VLSI	POWER ELECTRONICS
07-07-2025 (MONDAY)	20CE1005 FEM in Structural Engineering	20CS5005 Machine Learning	20ME3105 Advanced Heat Transfer	20EC4209 FPGA Architectures & Applications	20EE2112 Power Electronic Converters
09-07-2025 (WEDNESDAY)	20CE1006 Structural Dynamics	20CS5006 Soft Computing	20ME3106 Steam Engineering	20EC4210 Low Power VLSI Design	20EE2113 Digital control of Power Electronic and Drive Systems
11-07-2025 (FRIDAY)	20CE1013 Advanced Steel Design	20CS5019 Cyber Security	20ME3118 Refrigeration and Cryogenics	20EC4212 Algorithms for VLSI Design Automation	*****
14-07-2025 (MONDAY)	20CE1017 Design of Advanced Concrete Structures	20CS5021 Human Computer Interaction	20ME3123 Instrumentation for Thermal Engineering	20EC4104 Real Time Operating System	*****

Note: If any omissions or clashes in this time table may please be informed to the undersigned immediately.
CONTROLLER OF EXAMINATIONS**Controller of Examinations**
Siddharth Institute of Engineering & Technology
(AUTONOMOUS),
PUTTUR - 517583, Tirupati Dist. A.P.
PRINCIPAL**PRINCIPAL**
Siddharth Institute of Engineering & Technology
(AUTONOMOUS), Siddharth Nagar,
PUTTUR - 517 583, Tirupati Dist. A.P.